

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listing of claims in the application.

**Listing of Claims:**

1. (Currently Amended) A computing system ~~construction method under execution environment to be dependent on OS on the occasion of returning an error code to the application program in different execution environments of a computing system, wherein~~ comprising:

~~this computing system comprises~~ a CPU including a general register as hardware and executing an operating system ~~to be executed on the hardware;~~

wherein an instruction set of said CPU includes an instruction to load immediate value for storing an immediate value to said general register;

wherein said CPU executes an execution program ~~to be executed on said operating system consists of an execution program~~ corresponding to ~~said~~ an application program and a program ~~to be used to be independent on different OS to be~~

~~used in absorbing a different difference between~~ execution environments~~7, and outputs~~

~~an error code which is returned when control is returned~~  
~~is defined, from said program to be used to be independent on~~  
~~different OS, as the~~ a common error code to be independent on  
execution environment when the operation is returned from the  
program absorbing a difference between execution environments  
to in the execution program corresponding to said application  
program;

~~moreover, wherein~~ said common error code is ~~determined as~~  
a value within ~~the~~ a numerical range which can be set with  
said instruction to load immediate value~~7, and~~

~~said common error code~~ is held within the instruction  
code of said instruction to load immediate value.

2. (Currently Amended) A computing system ~~construction~~  
~~method under execution environment dependent on OS~~ according  
to claim 1, wherein said common error code is determined  
within ~~the~~ a range of a numerical value where the most  
significant bit (MSB) of said immediate value is set to zero  
(0).

3. (Currently Amended) A computing system ~~construction method under execution environment dependent on OS~~ according to claim 1, wherein said common error code is determined with a positive value without any sign,

wherein said CPU ~~under the condition that the zero promotion is~~ automatically performs a conducted sign extension for the leading part of data when data which is smaller than the number of bits of said general register ~~when such data is loaded to said general register as the specification of said instruction set.~~

4. (Currently Amended) A computing system ~~construction method under execution environment dependent on OS~~ according to claim 1, wherein said common error code is determined within a range of the numerical value to set the most significant bit MSB of said immediate value to zero (0),

wherein said CPU ~~under the condition that the sign promotion is~~ automatically performs a conducted sign extension for the leading part of data when data which is smaller than the number of bits of said general register ~~when such data is loaded to said general register as the specification of said instruction set.~~

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5. (Canceled)